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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,884	10/24/2003	Alfred I-Tsung Pan	10992304-3	1915
7590 09/06/2005			EXAMINER	
HEWLETT-PACKARD COMPANY Intellectual Property Administration P.O. Box 272400 Fort Collins, CO 80527-2400			GUERRERO, MARIA F	
			ART UNIT	PAPER NUMBER
			2822	

DATE MAILED: 09/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

HA

Office Action Summary	Application No.	Applicant(s)	
	10/692,884	PAN, ALFRED I-TSUNG	
	Examiner	Art Unit	
	Maria Guerrero	2822	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 6-17-05.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 14-36 is/are pending in the application.
- 4a) Of the above claim(s) 22-36 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 14-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This Office Action is in response to the Amendment filed June 17, 2005.

Status of Claims

2. Claims 1-13 are canceled. Claims 14-36 are pending.

Election/Restrictions

3. Newly submitted claims 22-36 are directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: the claims require among other steps: adhering chip substrates at different respective locations across a carrier substrate; lithographically processing the adhered chip substrates to form respective integrated structures; wherein ones the integrated structures are components of an inkjet printhead.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claims 22-36 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 14-18 and 20-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Wilson (US 4,890,157).

Wilson teaches adhering an unprocessed, integrateable form of a plurality of chips on the upper surface of a carrier substrate according to a first placement alignment precision (col. 7, lines 34-54). Wilson discloses lithographically processing the unprocessed, integrateable form of a plurality of chips on the upper surface (Abstract, col. 2, lines 35-68, col. 7, lines 34-54). Wilson shows the integrated chips being aligned with each other and the substrate with a second alignment precision having lithographic processing tolerances (col. 7, lines 34-54). Wilson describes the lithographic processing tolerance being ± 1 micron and the overlay tolerance of ± 1 mil. (col. 7, lines 34-54). Wilson teaches forming a plurality of alignment holes (slots) and adhering by using alignment pins to mate with corresponding the alignment holes (col. 2, lines 65-68, col. 3, lines 1-10, col. 6, lines 38-68, col. 7, lines 1-12). Wilson discloses using the casting method (curtain coating deposition) as part of the lithographically processing (col. 2, lines 45-47, col. 3, lines 65-68, col. 4, line 1). Wilson teaches the

upper surface of the integrated chips being directly formed in parallel, but different plane than the substrate carrier (col. 2, lines 35-4, col. 7, lines 15-50).

In addition, the elements must be arranged as required by the claim, but this is not an ipsissimis verbis test, i.e., identity of terminology is not required. In re Bond, 910 F.2d 831, 15 USPQ2d 1566 (Fed. Cir. 1990). Therefore, the claims are anticipated by Wilson.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Wilson (US 4,890,157) in view of Leibovitz et al. (5,055,425).

Regarding claim 19, Wilson does not specifically show polishing the upper surface of the plurality of chips. However, Leibovitz et al. discloses using polishing to remove surface irregularities and prepare the surface for the subsequent processing (col. 2, lines 35-38, col. 3, lines 20-30, 53-56, col. 4, lines 34-40).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to modify Wilson's process by including the step polishing as taught by Leibovitz et al. in order to remove surface irregularities and prepare the surface for the subsequent processing (Leibovitz et al., col. 2, lines 35-38).

Response to Arguments

6. Applicant's arguments filed June 17, 2005 have been fully considered but they are not persuasive. Claims 14-21 stand rejected.

7. Applicant argued that Wilson does not adhere unprocessed, integrateable form of a plurality of chips on the upper surface on the upper surface of a carrier substrate. However, a person of ordinary skill in the art would recognize that Wilson meets the claimed subject matter (Abstract, col. 2, lines 35-68, col. 7, lines 34-54).

8. Furthermore, during patent examination, the pending claims must be "given *>their< broadest reasonable interpretation consistent with the specification." > In re Hyatt, 211 F.3d 1367, 1372, 54 USPQ2d 1664, 1667 (Fed. Cir. 2000). While the claims of issued patents are interpreted in light of the specification, prosecution history, prior art and other claims, this is not the mode of claim interpretation to be applied during examination. During examination, the claims must be interpreted as broadly as their terms reasonably allow. > In re American Academy of Science Tech Center, F.3d, 2004 WL 1067528 (Fed. Cir. May 13, 2004)(The USPTO uses a different standard for construing claims than that used by district courts; during examination the USPTO must give claims their broadest reasonable interpretation.) < This means that the words of the claim must be given their plain meaning unless applicant has provided a clear definition in the specification. In re Zletz, 893 F.2d 319, 321, 13 USPQ2d 1320, 1322 (Fed. Cir. 1989) >; Chef America, Inc. v. Lamb-Weston, Inc., 358 F.3d 1371, 1372, 69 USPQ2d 1857 (Fed. Cir. 2004). Therefore, the terms unprocessed, integrateable form of plurality

chips have been interpreted as broadly as their terms reasonably allow in combination with the special definition provided in the specification (see specification page 5, lines 20-26).

9. Finally, "the use of patents as references is not limited to what the patentees describe as their own inventions or to the problems with which they are concerned. They are part of the literature of the art, relevant for all they contain." In re Heck, 699 F.2d 1331, 1332-33, 216 USPQ 1038, 1039 (Fed. Cir. 1983) (quoting In re Lemelson, 397 F.2d 1006, 1009, 158 USPQ 275, 277 (CCPA 1968)). A reference may be relied upon for all that it would have reasonably suggested to one having ordinary skill the art, including nonpreferred embodiments. Merck & Co. v. Biocraft Laboratories, 874 F.2d 804, 10 USPQ2d 1843 (Fed. Cir.), cert. denied, 493 U.S. 975 (1989). See also Celeritas Technologies Ltd. v. Rockwell International Corp., 150 F.3d 1354, 1361, 47 USPQ2d 1516, 1522-23 (Fed. Cir.1998).

Conclusion

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Eichhorn et al. (US 5,654,121) (of record) is cited as evidence to show casting methods included curtain coating deposition (col. 5, lines 40-41).

11. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within

TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria Guerrero whose telephone number is 571-272-1837.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

August 30, 2005


MARIA F. GUERRERO
PRIMARY EXAMINER